

REMARKS

Applicants are in receipt of the Office Action mailed July 14, 2004. Claims 1 – 31 were pending. Applicants have amended claims 2 – 5, 7, and 31. Claims 1 – 31 remain pending in the application.

Claims 1 – 30 were allowed by the Examiner. Applicants have made minor clarifying amendments to claims 2 – 5 and 7, for consistency with the remaining claims dependent upon claim 1.

Claim 31 was rejected under 35 U.S.C. §102(e) as being anticipated by Babella et al. (USPN 6,668,347, hereinafter “Babella”). Babella teaches an integrated circuit having a central built-in self-test unit that uses internal scan chains for testing embedded memory modules. (Abstract)

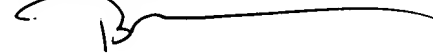
However, Applicants can find no teaching or suggestion in Babella of “An electronic memory device tester, comprising: input means for receiving seed data which has a first number, p, of seed data bits, from processing means **comprising a computer,**” as recited in Applicant’s amended claim 31. Accordingly, claim 31 is believed to patentably distinguish over the cited reference.

CONCLUSION

In light of the foregoing remarks, Applicant respectfully submits the application is now in condition for allowance, and an early notice to that effect is requested.

No fees are believed necessary; however , the Commissioner is authorized to charge any fees which may be required, or credit any overpayment, to Meyertons, Hood, Kivlin, Kowert, & Goetzel, P.C. Deposit Account No. 50-1505\5181-94100\BNK.

Respectfully submitted,



B. Noël Kivlin

Reg. No. 33,929

ATTORNEY FOR APPLICANT(S)

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.

P.O. Box 398

Austin, Texas 78767-0398

Phone: (512) 853-8800

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